REMARKS/ARGUMENTS

Claims 1-12 were pending in the present application. Claims 4-12 are allowed. The present response amends claim 1, leaving pending in the present application claims 1-12. Reconsideration of the pending claims is respectfully requested.

I.   Date of Office Action

Due to an error on behalf of the USPTO, the original Office Action was mailed to the incorrect address. A copy of the Office Action was later mailed to the correct address on November 30, 2004, with the period for response being restarted from that date. As such, Applicant respectfully submits that the present response is being submitted within the period to provoke an Advisory Action.

II.   Figures

Attached to the copy of the Office Action was a Notice of Draftsperson's Patent Drawing Review indicating objections to the Figures that will be required when deemed necessary by the Examiner. As no reference to the Figures was present in the Office Action, it is assumed that these corrections are not necessary at the present time. If this assumption is incorrect, Applicant respectfully requests that the Examiner contact the undersigned representative for the Applicant in order to avoid unintentional abandonment of the present application.

III.   Allowed Claims

Claims 4-12 are allowed.

IV.   Rejection under 35 U.S.C. §102

Claims 1 and 2 are rejected under 35 U.S.C. §102(b) as being anticipated by *Nayebi* (US 5,689,309). Applicant's claim 1 requires a high speed video mixer circuit, defined by:

> a reference circuit generating a DC bias voltage; and
> **an emitter-coupled differential pair receiving as input signals a video signal and said DC bias voltage**, said differential pair providing a **first output video signal corresponding substantially to an AC portion of said video signal**, said differential pair further providing a **separate second output video signal amplifying said video signal about said DC bias voltage, whereby a voltage level of the first output video signal corresponds to a blanking signal for the second output video signal**

(*emphasis added*). Such limitations are not disclosed by *Nayebi*.

Nayebi discloses first and second differential pairs, where a first one of the differential pairs receives as input a composite video signal and a supply voltage Vcc, as noted in the Office Action on p. 3 (Fig. 6; col. 6, line 52-col. 7, line 57). These differential pairs are part of a "Gilbert cell which accepts the differential input signals, mixes them in a ratio controlled by the content signals CNTR and CNTD and outputs a differential output signal" (col. 6, lines 61-67). Neither of these differential pairs of *Nayebi* receives as input a video signal and a DC bias voltage, then provides a "first output video signal corresponding substantially to an AC portion of said video signal" as well as "providing a separate second output video signal amplifying said video signal about said DC bias voltage," whereby "a voltage level of the first output video signal corresponds to a blanking signal for the second output video signal" as required by Applicant's claim 1

As discussed above, these differential pairs of *Nayebi* are used together to output a single differential output signal. *Nayebi* does not disclose a differential pair providing separate first and second video signals. The subsequent amplified signal "carried out by driver 66" in *Nayebi* (as cited by the Examiner) is not a separate video signal provided by the differential pair, as required by claim 1, but is an amplified version of the same differential video signal as provided by the output driver circuit (Fig. 6; col. 6, line 52-col. 7, line 57).

Nayebi also does not disclose a differential pair providing "a first output video signal corresponding substantially to an AC portion of said video signal," which "corresponds to a blanking signal for the second output video signal" as required by Applicant's claim 1. As discussed above, *Nayebi* instead mixes the differential input signals "in a ratio controlled by the content signals CNTR and CNTD" (col. 6, lines 52-67). *Nayebi* does not disclose, teach, or suggest a differential pair providing a first output video signal that corresponds to an AC portion of the input video signal, which further corresponds to a blanking signal for a second output video signal also provided by the differential pair as required by claim 1.

As *Nayebi* does not disclose each element of Applicant's claim 1, *Nayebi* cannot anticipate Applicant's claim 1. As claim 2 depends from claim 1, neither can claim 2 be anticipated. Applicant therefore respectfully requests that the rejection with respect to claims 1 and 2 be withdrawn.

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V.   Rejection under 35 U.S.C. §103

Claim 3 is rejected under 35 U.S.C. §103(a) as being obvious over *Nayebi*. Claim 3 depends from claim 1. As discussed above, *Nayebi* does not disclose, teach, or suggest all limitations of Applicant's claim 1. As *Nayebi* cannot render claim 1 obvious, neither can dependent claim 3 be rendered obvious by *Nayebi*. Applicant therefore respectfully requests that the rejection with respect to claim 3 be withdrawn.

VI.   Amendments to the Claims

The amendments to the claims are made for purposes of clarity, and unless otherwise noted are not intended to alter the scope of the claims or limit any equivalents thereof. The amendments are fully supported by, and do not add new matter to, the specification.
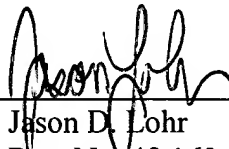
VII.   Conclusion

In view of the above, it is respectfully submitted that the application is now in condition for allowance. Reconsideration of the pending claims, consideration of the newly presented claims, and a notice of allowance is respectfully requested.

The Commissioner is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 50-1703, under Order No. NSC1-H0700. **A duplicate copy of this sheet is enclosed.**

Respectfully submitted,

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Dated: December 21 , 2004   By:_____

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